

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS PO. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 6162

	SERIAL NUMB 10/737,280	ERIAL NUMBER 10/737,280 FILING OR 371(c) DATE 12/16/2003 RULE		CLASS 714		GROUP ART UNIT 2133		ATTORNEY DOCKET NO. X-1188 US			
	APPLICANTS Stephen M. Trimberger, San Jose, CA; ** CONTINUING DATA **********************************										
o' '	FOREIGN APPLICATIONS ************************************										
				r tials	STATE OR COUNTRY CA	DRA	SHEETS DRAWING 12		TAL NIME ()	INDEPENDENT CLAIMS	
	ADDRESS 24309 TITLE Methods and structures for providing programmable width and error correction in memory arrays in programmable logic devices										
	RECEIVED	No	: Authority has been given in Paper to charge/credit DEPOSIT ACCOUNT for following:				☐ All Fees ☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of time) ☐ 1.18 Fees (Issue) ☐ Other ☐ Credit				